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L2: (87) (POECHMUELLER near2 PETER).in.
L3: (29610) stor$4 near5 transistor
L4: (4061) amplif$8 same 3
L5: (119949) DRAM or (dynamic adj3 memory)
L6: (546) 4 same 5
L7: (16599) (source or drain) with fixed with (potential or voltage)
L8: (11) 6 same 7
L9: (30) 4 same 7
L10: (19) 9 not 8
L11: (87575) amplif$8 near5 transistor
L12: (1456) 3 same 11
L13: (151117) bitline or (bit adj line) or bit-line or dataline or (data adj line) or data-line
L14: (316) 12 same 13
L15: (52) 5 same 14
L16: (48) 15 not 9
L19: (4281) vertical adj2 transistor
L20: (208) 11 same 19
L21: (6) 3 same 20
L22: (881) amplif$8 near5 (single adj2 transistor)
L23: (8) 19 and 22
L24: (16) 7 same 12
L25: (598) 7 same 11
L27: (65) 7 and 22
L28: (16) 3 same 25
L29: (245) 4 and 7
L30: (76) 5 and 29
L31: (109) 7 and 12
L32: (66) 31 not (30 or 27 or 15 or 9)
L34: (29) 33 not (31 or 30 or 27 or 15 or 9)
L39: (19611) 365/20?
L40: (39) 22 and 39
L41: (84154) "365"/$6
L42: (63) 22 and 41
L43: (24) 42 not 40
L33: (44) 5 and 22
L44: (15325) (amplif$8 near5 transistor).clm.
L45: (1301) 44 and 3
L46: (1208) 44 and 5
L47: (2141) 45 or 46
L49: (176) 7 and 47

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